

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
:)
KEISHI SAITO, ET AL.)
:)
Application No.: Not Yet Assigned)
:)
Filed: Concurrently Herewith)
:)
For: SEMICONDUCTOR ELEMENT)
AND ITS MANUFACTURING)
METHOD) July 23, 2003

**This application is a divisional
under 37 C.F.R. § 1.53(b) of
U.S. Patent Appln. No. 09/839,891**

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

Preliminary to examination, please amend the above-identified application,
as follows: